			ļ		
L	Hits	Search Text	DB	Time stamp	
Number					
1	2485	(memory storage) near3 (module bank	USPAT;	2003/09/04	
		sector) and ((cache storage memory) with	US-PGPUB;	16:27	
		tag)	EPO; JPO;		
			DERWENT		
2	371	((storage cache memory) adj4 (controller	USPAT;	2003/09/04	
		logic)) and way and ((evict\$3 victim) near3	US-PGPUB;	16:26	
ļ		(line entry buffer queue register))	EPO; JPO;		
		•	DERWENT		
3	120	((memory storage) near3 (module bank	USPAT;	2003)09/04	
		sector) and ((cache storage memory) with	US-PGPUB;	16:29	
		tag)) and (((storage cache memory) adj4	EPO; JPO;	, '	
		(controller logic)) and way and ((evict\$3	DERWENT		
		victim) near3 (line entry buffer queue			
		register)))			
4	₹106	711/\$.ccls. and (((memory storage) near3	USPAT;	2003/09/04	
	,-	(module bank sector) and ((cache storage	US-PGPUB;	16:29	
-		memory) with tag)) and (((storage cache	EPO; JPO;		
		memory) adj4 (controller logic)) and way	DERWENT		
	•	and ((evict\$3 victim) near3 (line entry			
		buffer queue register))))			
5	946	(memory storage) near3 (module bank	USPAT;	2003/09/04	
·		sector) and ((cache storage memory) with	US-PGPUB;	16:28	
	•,	tag) and ((stor\$3 writ\$3) adj3 back)	EPO; JPO;		
			DERWENT		
6	102	(((storage cache memory) adj4 (controller	USPAT;	2003/09/04	
ŀ		logic)) and way and ((evict\$3 victim) near3	US-PGPUB;	16:29	
		(line entry buffer queue register))) and	EPO; JPO;		
		((memory storage) near3 (module bank	DERWENT		
		sector) and ((cache storage memory) with			
		tag) and ((stor\$3 writ\$3) adj3 back))		1	

L	Hits	Search Text	DB	Time stamp
Number 8	7366	((memory cache storage) adj4 (controller	USPAT;	2003/09/04
8	7300	logic circuit)) same (cache memory	US-PGPUB;	16:53
		storage) near4 (index tag field)	EPO; JPO;	
		storage) near4 (index tay ileid)	DERWENT	
_		(for a second at a second a second (book module	USPAT;	2003/09/04
9	144	((memory storage) near3 (bank module	US-PGPUB;	17:05
		sector block)) and ((victim eviction) near4		17.05
		(line entry cache buffer queue register)) and	EPO; JPO;	
		((memory storage) near3 (module bank	DERWENT	4
		sector) and ((cache storage memory) with		)_
		tag))		`}
10	33	(decoder deserializer) and (((memory	USPAT;	2003/09/04
		storage) near3 (bank module sector block))	US-PGPUB;	17:05
		and ((victim eviction) near4 (line entry	EPO; JPO;	
i		cache buffer queue register)) and ((memory	DERWENT	
	Į.	storage) near3 (module bank sector) and		
	7.	((cache storage memory) with tag)))		
11	452	(sequencer serializer) and (((memory cache	USPAT;	2003/09/04
	,	storage) adj4 (controller logic circuit))	US-PGPUB;	17:04
		same (cache memory storage) near4 (index	EPO; JPO;	
		tag field))	DERWENT	
12	322	(decoder deserializer) and ((sequencer	USPAT:	2003/09/04
12	322	serializer) and (((memory cache storage)	US-PGPUB;	17:05
		adi4 (controller logic circuit)) same (cache	EPO; JPO;	
			DERWENT	
4.0		memory storage) near4 (index tag field)))	USPAT;	2003/09/04
13	374	((memory storage) near3 (bank module	US-PGPUB;	17:06
		sector block)) and ((sequencer serializer)		17.00
		and (((memory cache storage) adj4	EPO; JPO;	· ·
		(controller logic circuit)) same (cache	DERWENT	
•	٠.	memory storage) near4 (index tag field)))		
14	255	((memory storage) near3 (bank module	USPAT;	2003/09/04
		sector block)) and ((decoder deserializer)	US-PGPUB;	17:06
		and ((sequencer serializer) and (((memory	EPO; JPO;	
	Į.	cache storage) adj4 (controller logic	DERWENT	
		circuit)) same (cache memory storage)		
		near4 (index tag field))))		

L	Hits	Search Text	DB	Time stamp	
Number					
1	49	(((system memory storage) adj3 (controller logic circuit)) with ((tag index field) adj3	USPAT; US-PGPUB;	2003/09/04 19:54	
İ		(buffer array register))) same ((memory	EPO; JPO;		
		storage) adj3 (bank module sector block))	DERWENT		